Fig. 1

INPUT	OUTPUT
0000000X	111
000000 1X	110
00000 1XX	101
0000 1XXX	100
000 1XXXX	011
00 1XXXXX	010
01XXXXXX	001
1XXXXXXX	000

Fig. 3

INPUT	OUTPUT
000X	00
001X	01
01XX	10
1XXX	11

Fig. 4

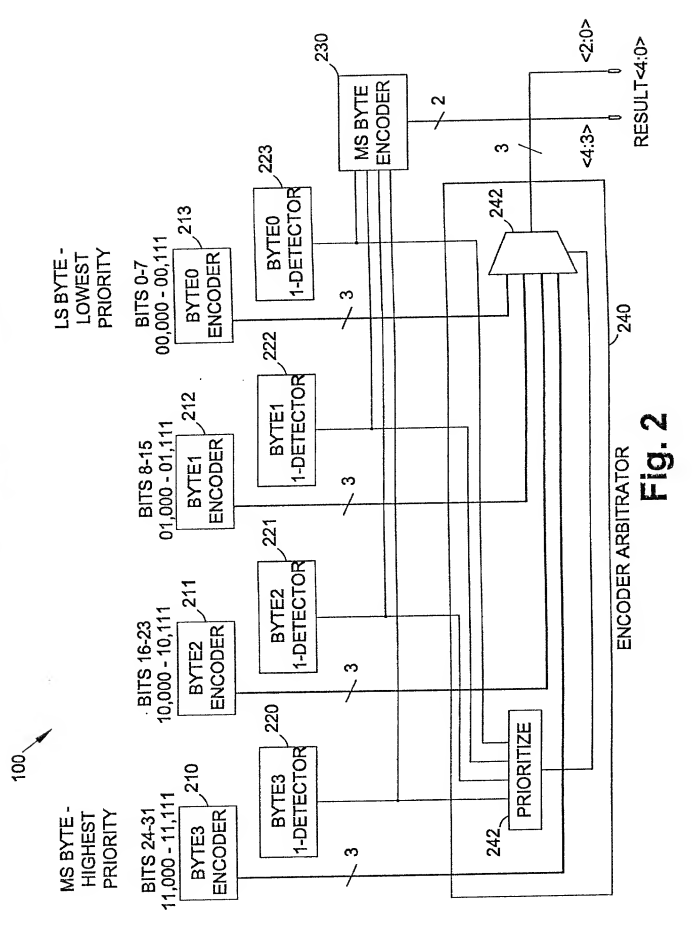
100
MS BYTE - HIGHEST PRIORITY
LS BYTE - LOWEST PRIORITY
BITS 24-31 11,000 - 11,111
BYTE3 ENCODER
210
BITS 16-23 10,000 - 10,111
BYTE2 ENCODER
211
BITS 8-15 01,000 - 01,111
BYTE1 ENCODER
212
BITS 0-7 00,000 - 00,111
BYTE0 ENCODER
213
BYTE3 1-DETECTOR
220
BYTE2 1-DETECTOR
221
BYTE1 1-DETECTOR
222
BYTE0 1-DETECTOR
223
3
PRIORITIZE
242
ENCODER ARBITRATOR
240
MS BYTE ENCODER
230
2
<2:0>
<4:3>
RESULT<4:0>

Fig. 2

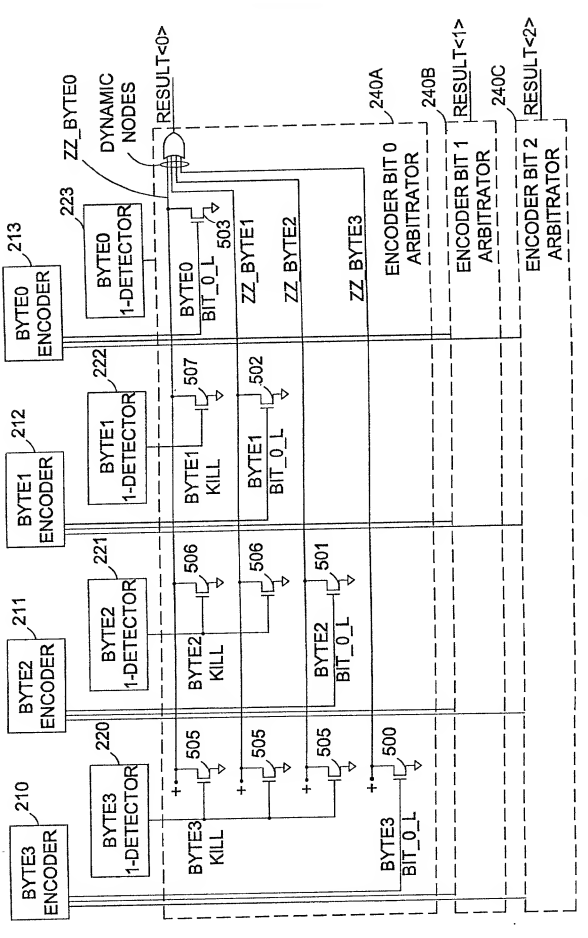
BYTE3 ENCODER
210
BYTE3 1-DETECTOR
220
BYTE2 ENCODER
211
BYTE2 1-DETECTOR
221
BYTE1 ENCODER
212
BYTE1 1-DETECTOR
222
BYTE0 ENCODER
213
BYTE0 1-DETECTOR
223
BYTE3 KILL
BYTE3 BIT_0_L
BYTE2 KILL
BYTE2 BIT_0_L
BYTE1 KILL
BYTE1 BIT_0_L
BYTE0 BIT_0_L
500
501
502
503
505
506
507
ZZ_BYTE0
ZZ_BYTE1
ZZ_BYTE2
ZZ_BYTE3
DYNAMIC NODES
RESULT<0>
RESULT<1>
RESULT<2>
ENCODER BIT 0 ARBITRATOR
240A
ENCODER BIT 1 ARBITRATOR
240B
ENCODER BIT 2 ARBITRATOR
240C

Fig. 5

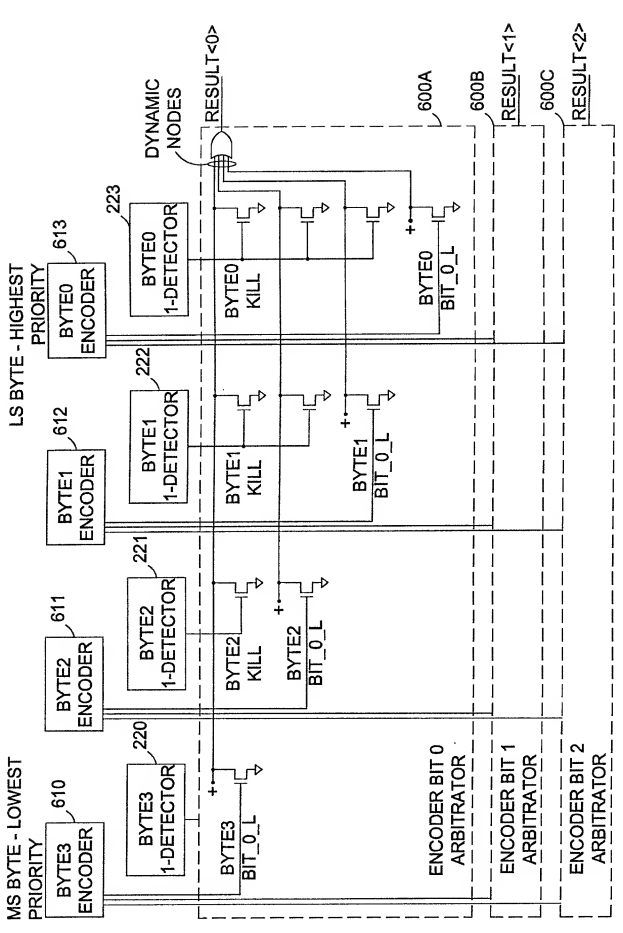
MS BYTE - LOWEST PRIORITY
LS BYTE - HIGHEST PRIORITY
DYNAMIC NODES
BYTE3 ENCODER
610
BYTE3 1-DETECTOR
220
BYTE3 BIT_0_L
BYTE2 ENCODER
611
BYTE2 1-DETECTOR
221
BYTE2 KILL
BYTE2 BIT_0_L
BYTE1 ENCODER
612
BYTE1 1-DETECTOR
222
BYTE1 KILL
BYTE1 BIT_0_L
BYTE0 ENCODER
613
BYTE0 1-DETECTOR
223
BYTE0 KILL
BYTE0 BIT_0_L
RESULT<0>
RESULT<1>
RESULT<2>
600A
600B
600C
ENCODER BIT 0 ARBITRATOR
ENCODER BIT 1 ARBITRATOR
ENCODER BIT 2 ARBITRATOR

Fig. 6

INPUT	OUTPUT
XXXXXXX1	111
XXXXXX10	110
XXXXX100	101
XXXX1000	100
XXX10000	011
XX100000	010
X1000000	001
X0000000	000

Fig. 7

INPUT	OUTPUT
XXX1	00
XX10	01
X100	10
X000	11

Fig. 8

INPUT	OUTPUT
11111111X	111
1111110X	110
111110XX	101
11110XXX	100
1110XXXX	011
110XXXXX	010
10XXXXXX	001
0XXXXXXX	000

Fig. 9

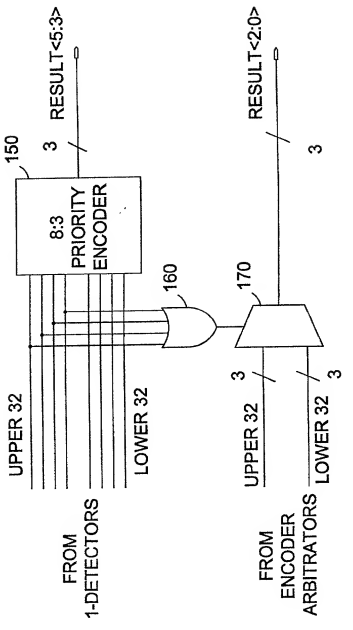
150
3
RESULT<5:3>
8:3
PRIORITY
ENCODER
160
170
RESULT<2:0>
3
UPPER 32
LOWER 32
FROM
1-DETECTORS
3
3
UPPER 32
LOWER 32
FROM
ENCODER
ARBITRATORS

Fig. 10